IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Halahan, Patrick B.; Siniaguine, Oleg

Assignee:

Title:

Tru-Si Technologies, Inc.

Semiconductor Structures Having Multiple Conductive Layer for Some

Opening, And Methods For Fabricating Same

Serial No.:

Not yet known

Filing Date:

Examiner:

Not yet known

Group Art Unit: Not yet known

Docket No.:

M-9999-1D US

San Jose, California August 28, 2001

Box Patent Application COMMISSIONER FOR PATENTS Washington, D. C. 20231

PRELIMINARY AMENDMENT

Dear Sir:

Please amend the above-identified application as follows.

IN THE SPECIFICATION

On page 1, before line 6, please insert the following paragraphs:

CROSS-REFERENCE TO RELATED APPLICATIONS

The present application is a division of U.S. patent application no. 09/792,311 filed February 22, 2001, incorporated herein by reference.

IN THE CLAIMS

Please cancel Claims 13-19 and 23-29 without prejudice.

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